

FIG. 1

**Transmitter**  
 - power supplies  
 - impedance level

**Signal**  
 - swing  
 - current/voltage  
 - common mode  
 - differential/single-ended

**Receiver**  
 - power supplies  
 - impedance level

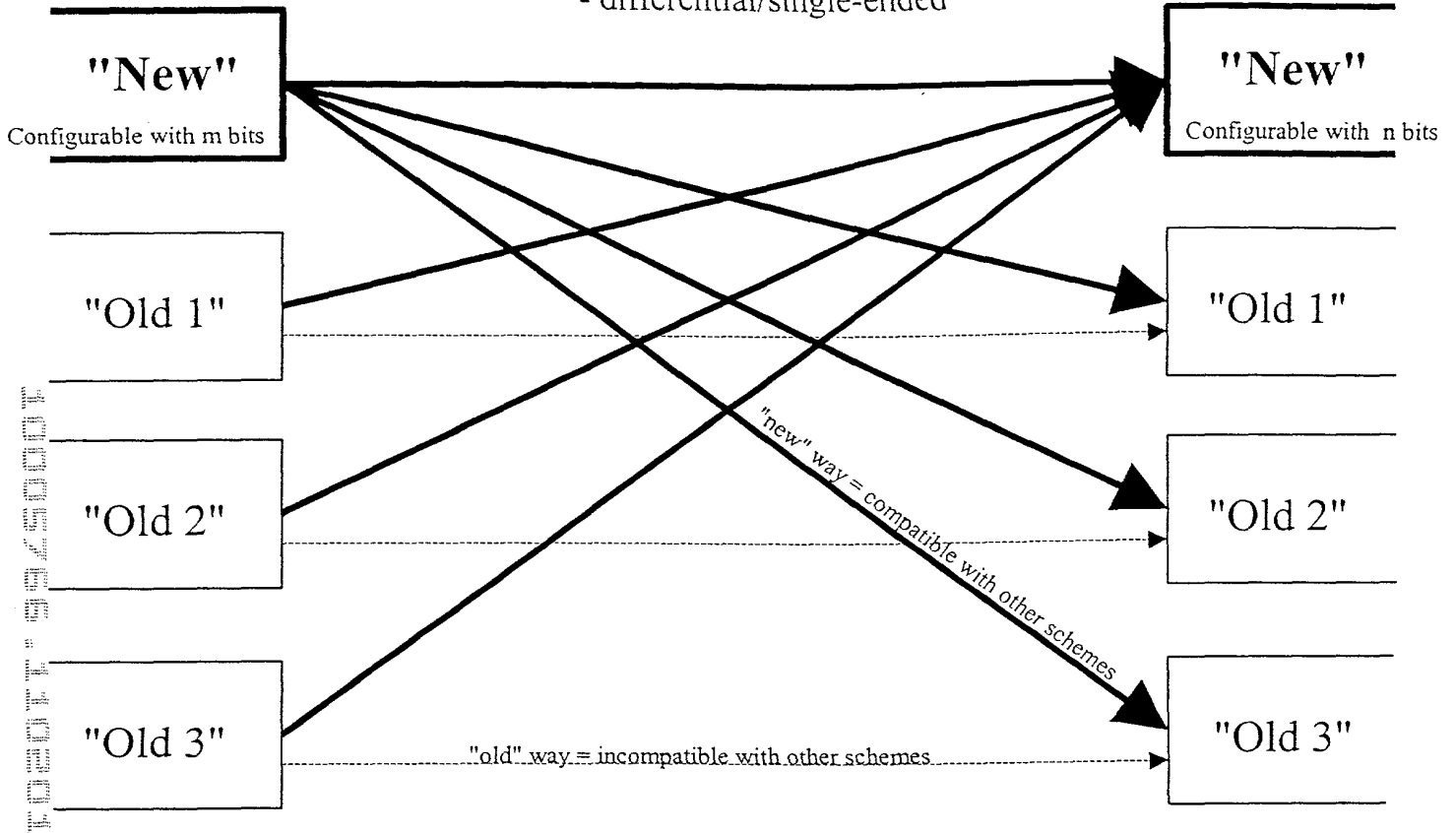


Fig. 2

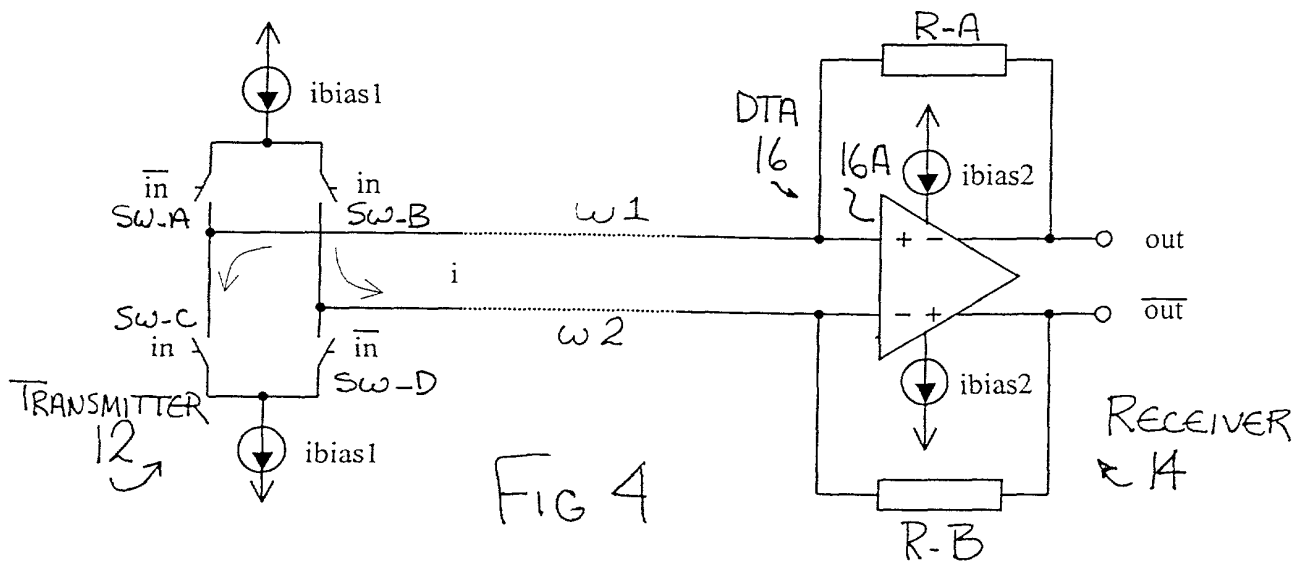
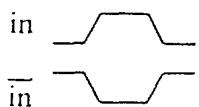
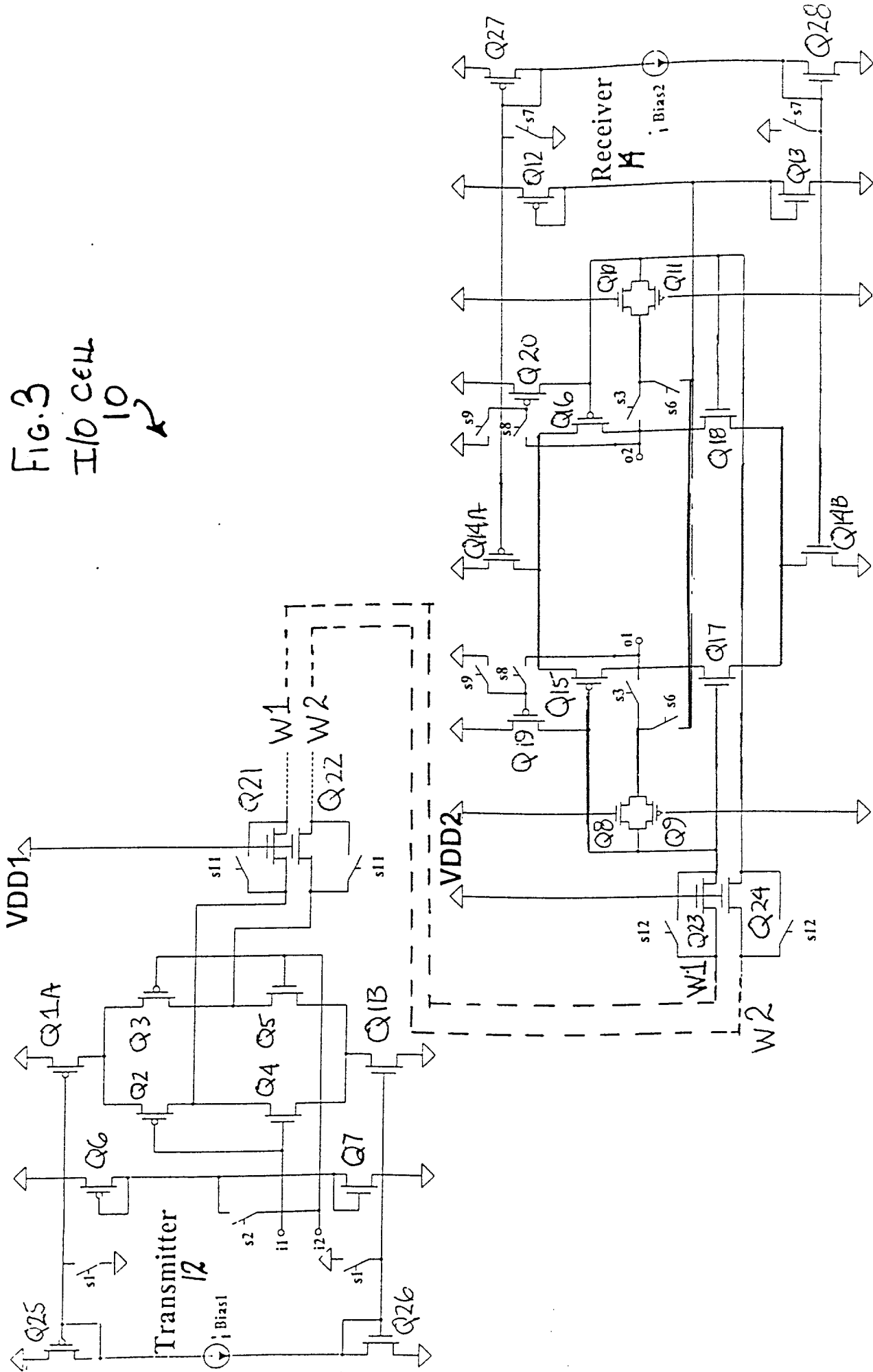


FIG 4





[illegible]

Two single-ended links:  
(current or voltage mode)

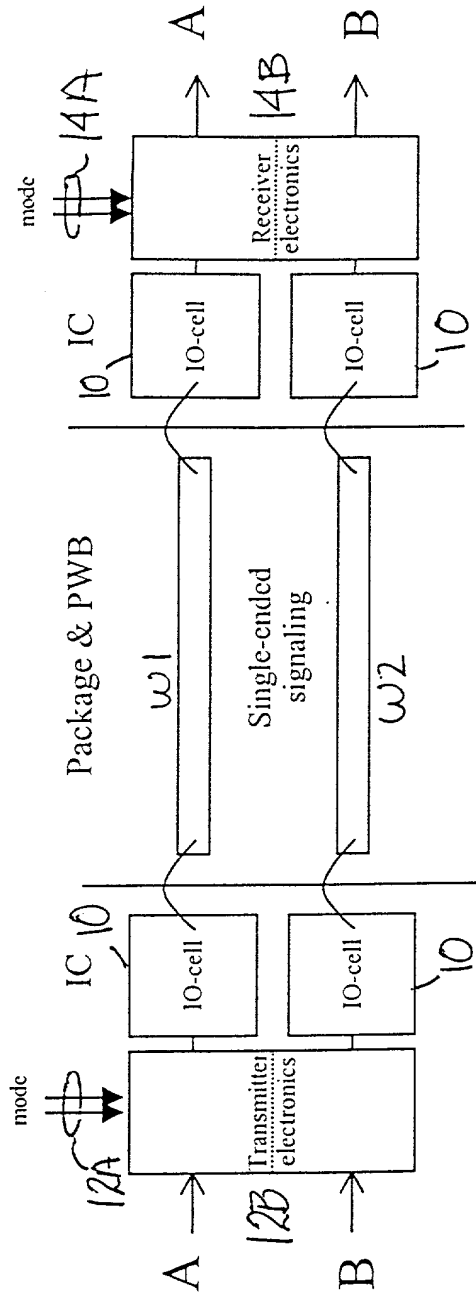


Fig. 5A

Single differential link:  
(current or voltage mode)

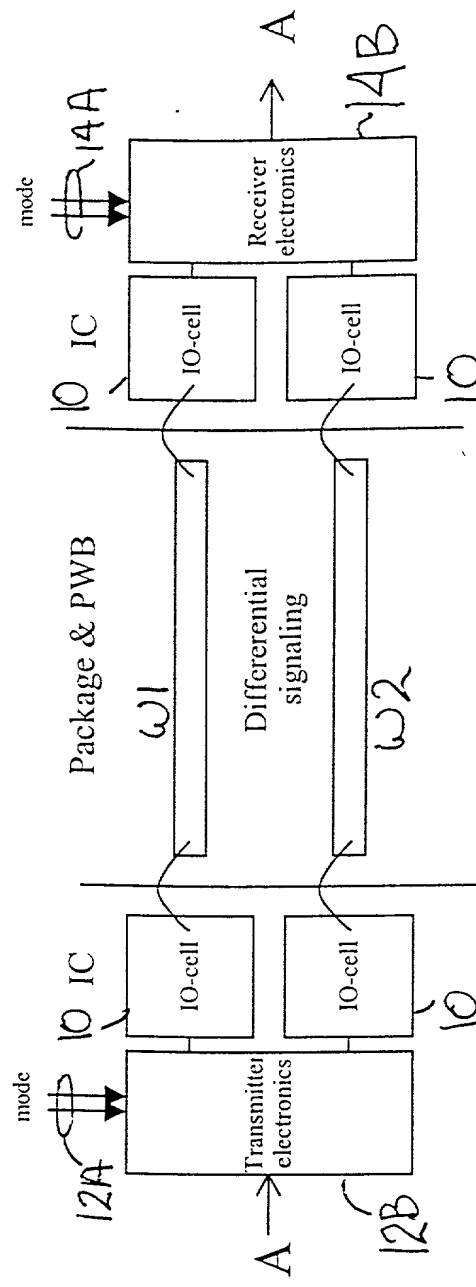
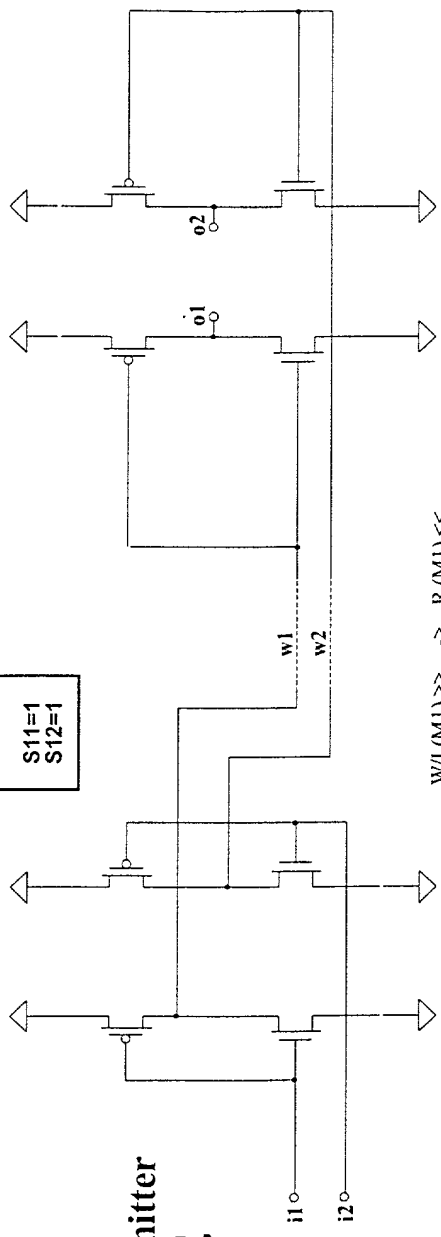


Fig. 5B

Patent No. 5,325,000

Transmitter 12 Receiver 14



W/L(M1) >> R (M1) <<  
W/L(M2) >> R (M2) <<  
W/L(M3) >> R (M3) <<  
W/L(M4) >> R (M4) <<

Fig. 6

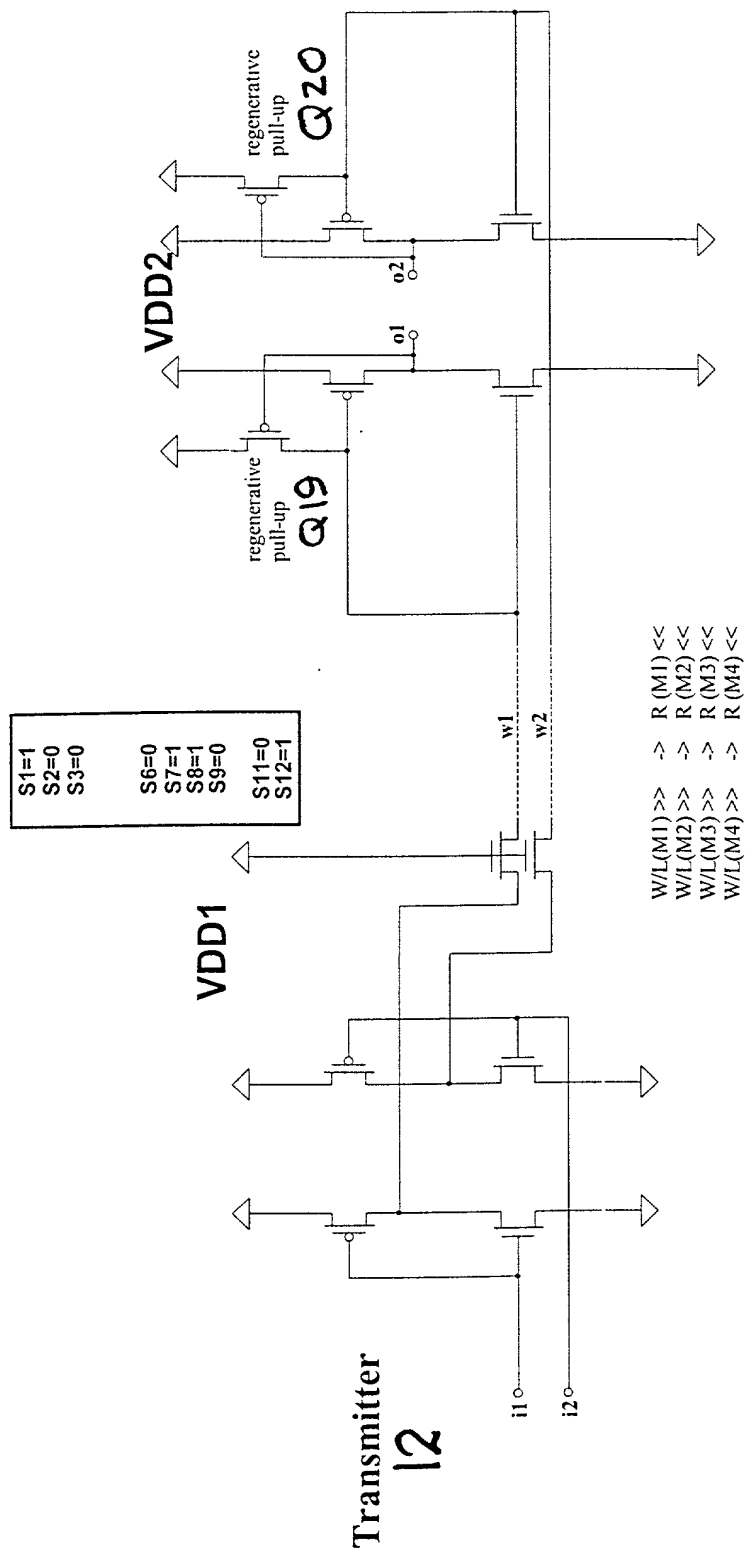


Fig. 7

33450001

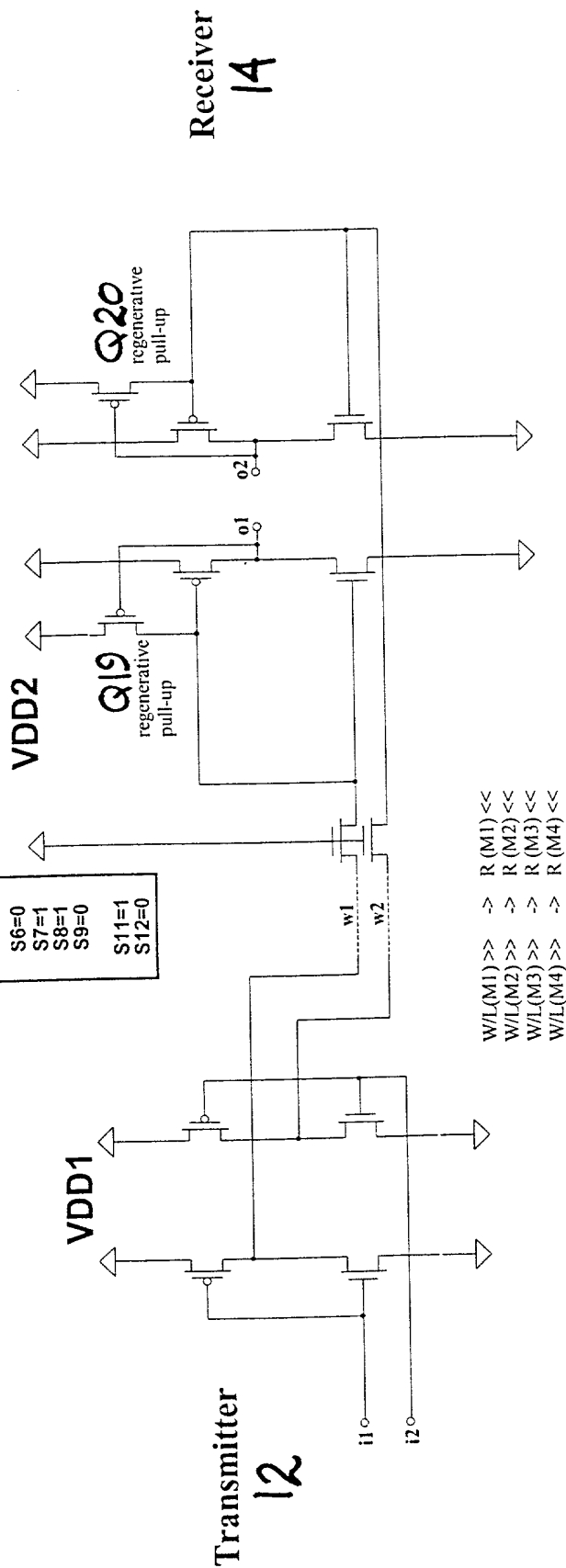


Fig. 8

93/5000

VDD1

Transmitter  
12

Receiver  
14

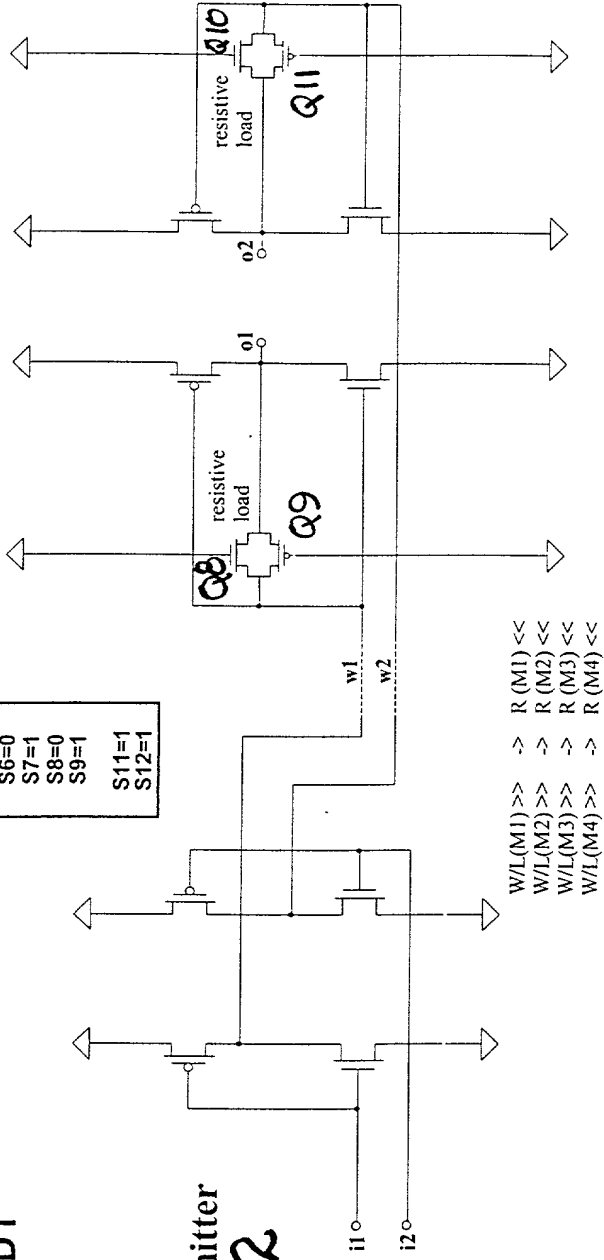


Fig. 9

W/L(M1)>>> R (M1)<<<  
W/L(M2)>>> R (M2)<<<  
W/L(M3)>>> R (M3)<<<  
W/L(M4)>>> R (M4)<<<





S1=0	S6=1	S11=1
S2=0	S7=0	S12=1
S3=0	S8=0	
	S9=1	

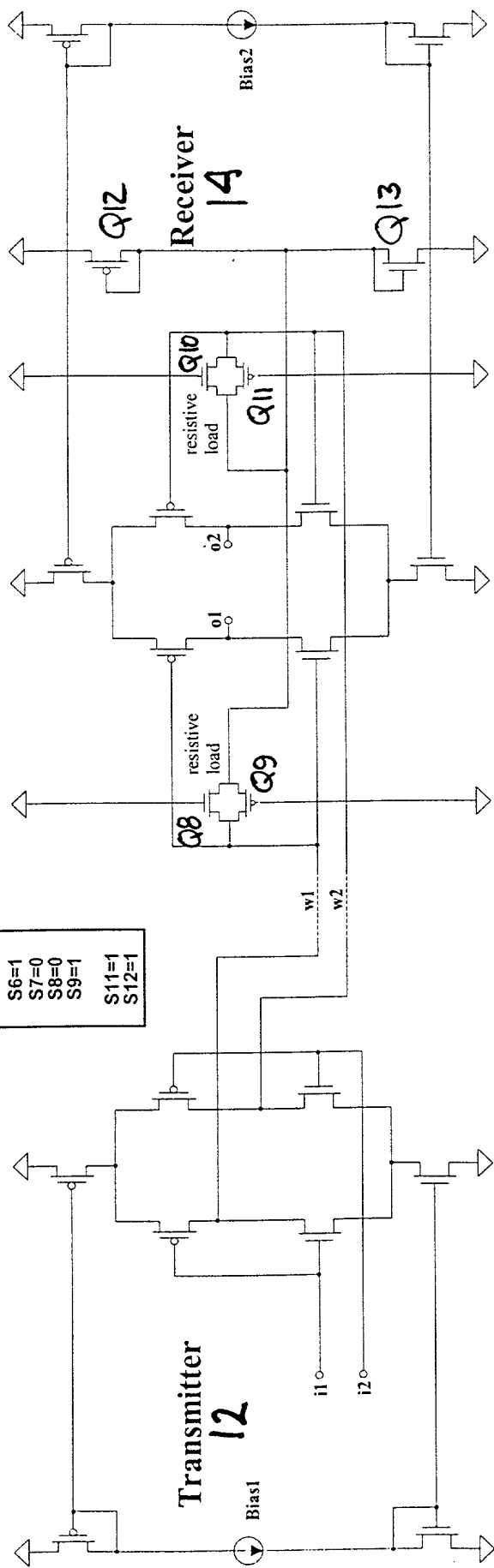


Fig. 11

S1=0  
S2=1  
S3=1  
  
S6=0  
S7=0  
S8=0  
S9=1  
  
S11=1  
S12=1

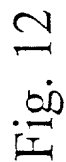


Fig. 12

S1=0
S2=0
S3=1
S6=0
S7=0
S8=0
S9=1
S11=1
S12=1

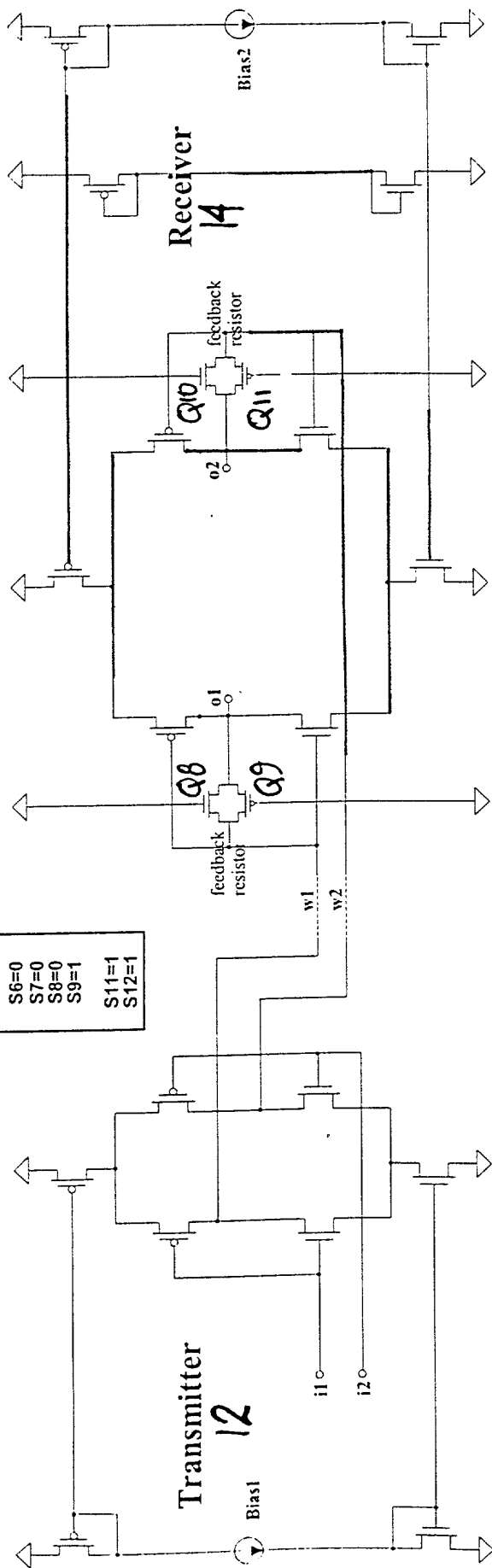


Fig. 13

6001 335000

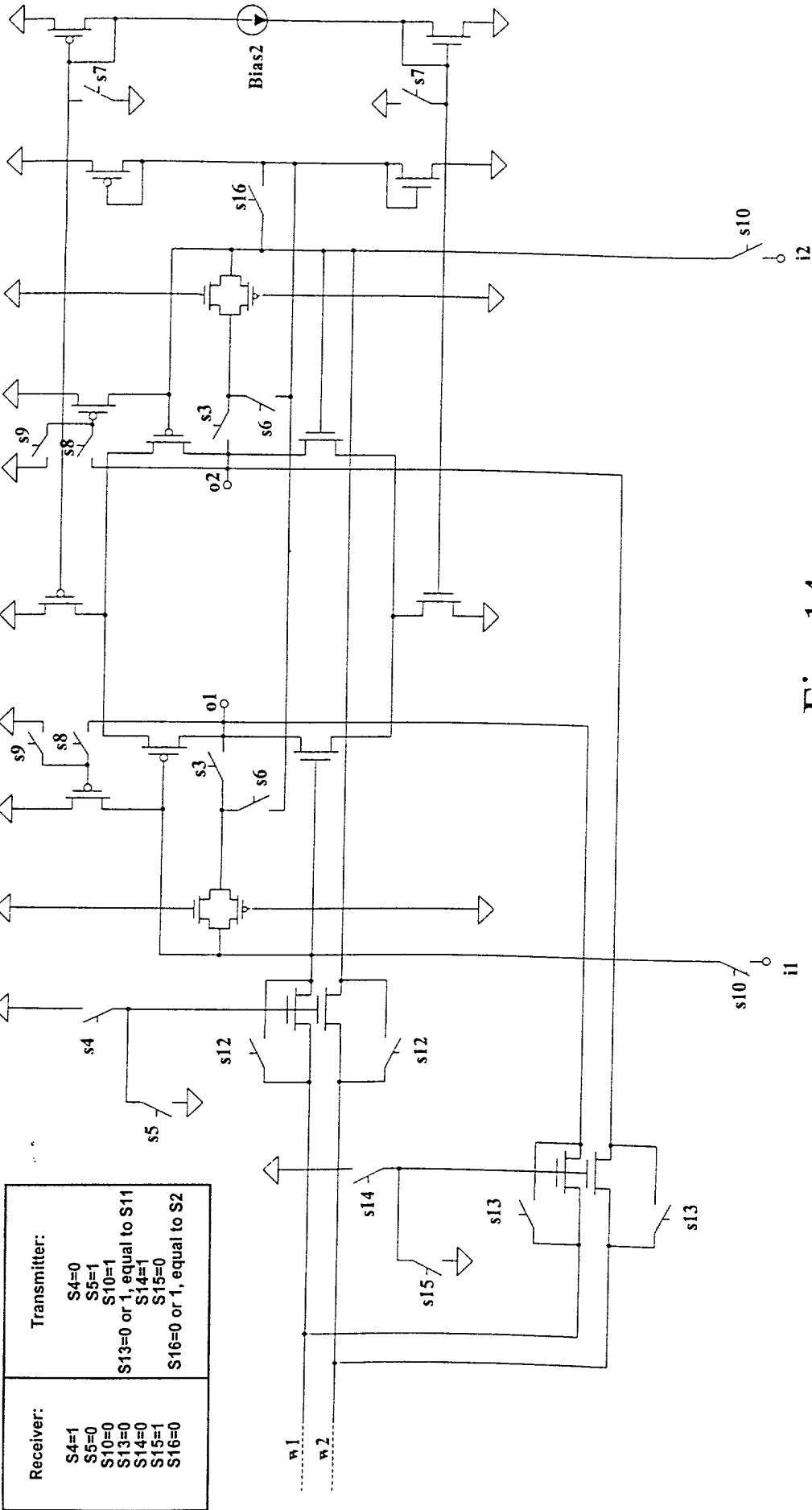


Fig. 14